# Lab 6: Medium-scale Integrated Circuit

## 1 Introduction

The purpose of this assignment is to learn the function of 74LS181and to use seven-segment displays and seven-segment decoder

## 2 Equipment Required

Digital Logic Trainer

1 each, 74181 integrated circuits

## 3 Instructions

Use seven-segment displays to show the result of 74LS181

3.1 Draw the logic diagram.

3.2 Result

If A=7, B=e

|  |  |
| --- | --- |
| function | result |
| A add B |  |
| A-B |  |
| A·B |  |
| /(A⊕B) |  |
| （A+/B) add A add 1 |  |

## 4 Knowledge Gains from Experiment